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FIG. 1A

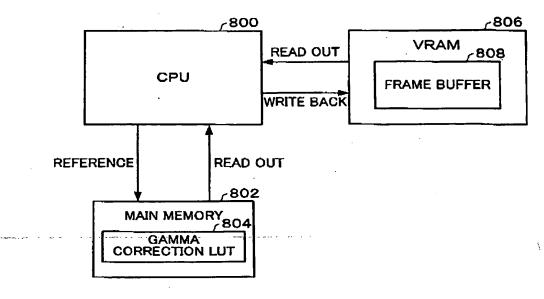
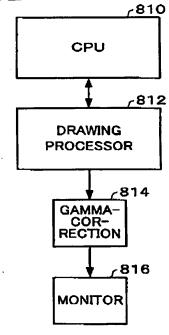


FIG. 1B



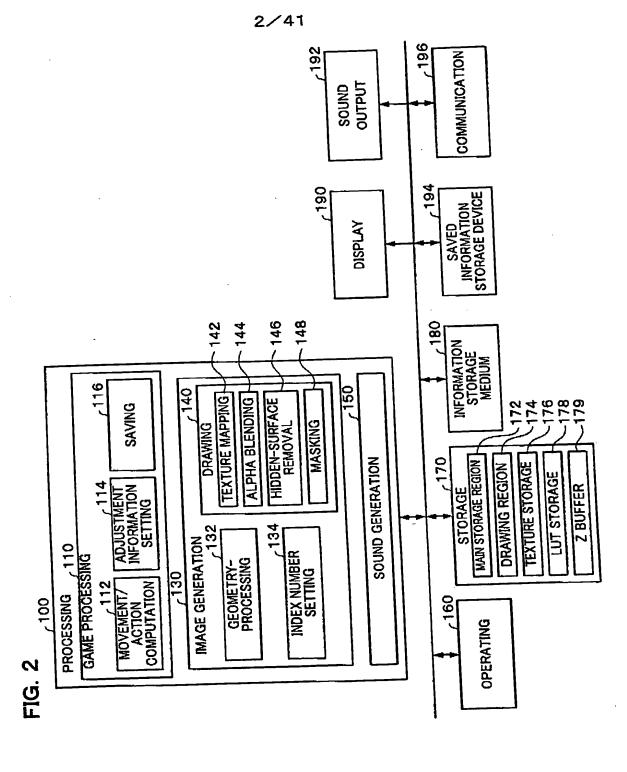
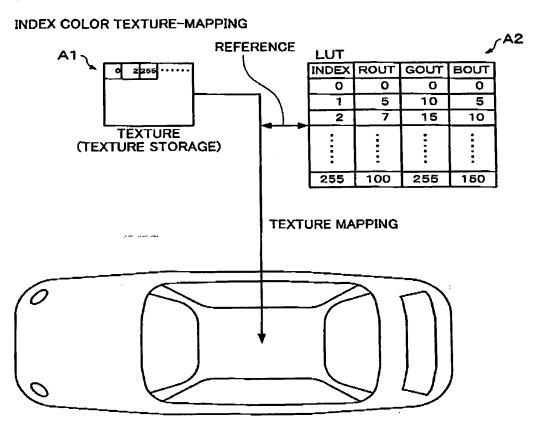


FIG. 3



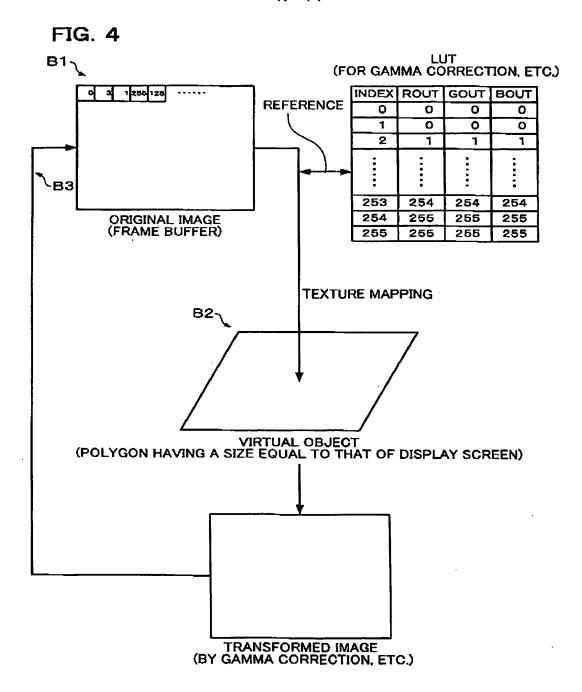


FIG. 5A

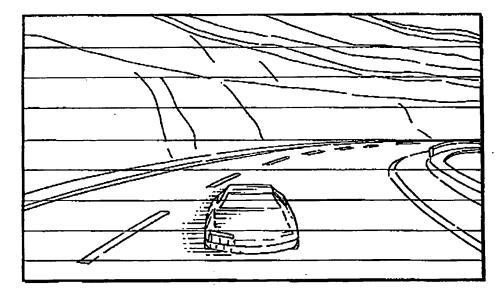
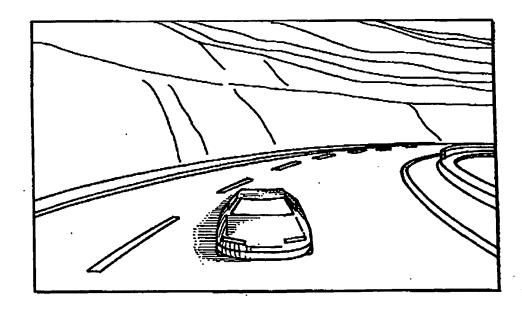
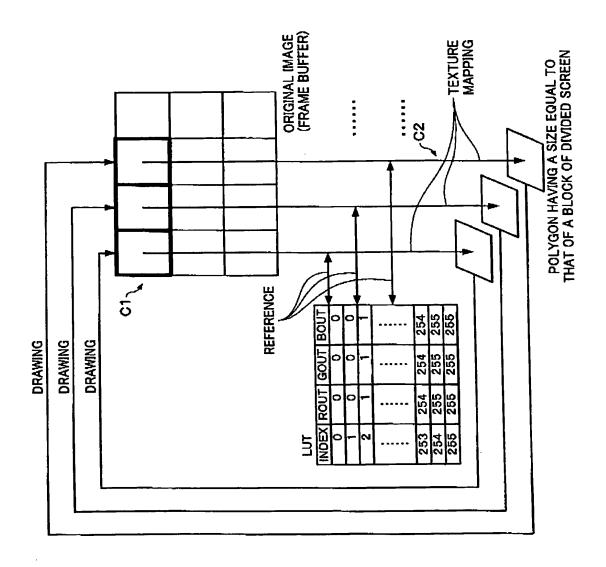


FIG. 5B





F1G. 6

FIG. 7A

GAMMA CORRECTION

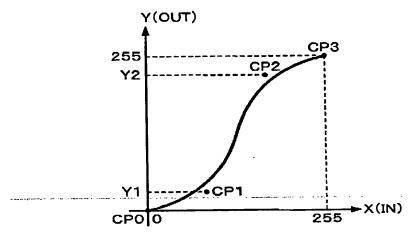


FIG. 7B

LUT FOR GAMMA CORRECTION

INDEX	ROUT	GOUT	BOUT
0	0	0	0
1	Ö	0	0
2	1	1	1
3	1	1	1
4	2	2	2
5	3	3	3
		:	
250	252	252	252
251	253	253	253
252	254	254	254
253	254	254	254
254	255	255	255
255	255	255	255



FIG. 8A

NEGATIVE/POSITIVE INVERSION

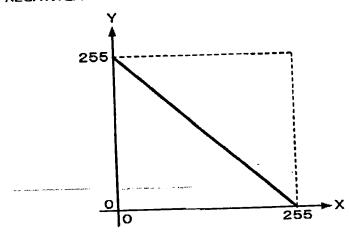


FIG. 8B

LUT FOR NEGATIVE/POSITIVE INVERSION

INDEX	ROUT	GOUT	BOUT
INDEX	255	255	255
<u> </u>	254	254	254
<u> </u>	253	253	253
2	252	252	252
3	252	2.02	
:	1	1	1 :
	2	 	2
253	2		1
254	<u> </u>		
255	0		1

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FIG. 9A POSTERIZATION

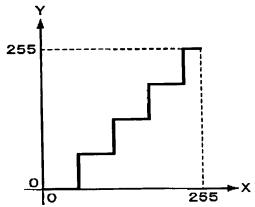


FIG. 9B SOLARIZATION

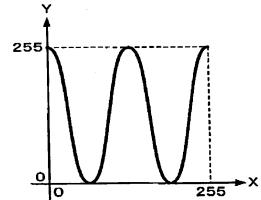


FIG. 9C BINARIZATION

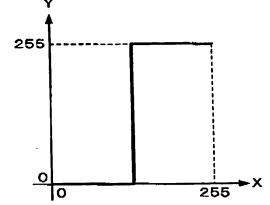


FIG. 10A

LUTR FOR MONOTONE FILTERING

INDEX	ROUTR	GOUTR	BOUTR
0	0	0	0
1	0	0_	0
2	0	0	0
3	0	0	0
4	1	1	1
5	1	1	1
:	:	•	•
250	74	74	74
251	75	75	75
252	75_	75	75
253	75	75	75
254	75	75	75
255	76	76	76

FIG. 10B

LUTG FOR MONOTONE FILTERING

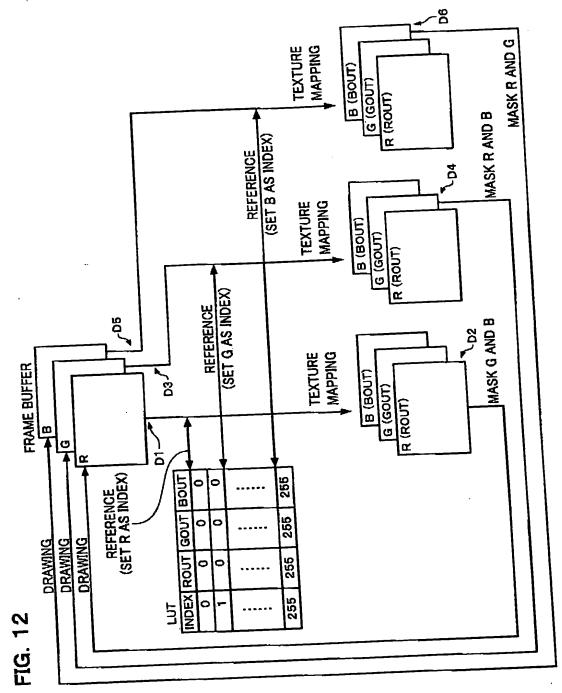
INDEX	ROUTG	GOUTG	BOUTG
0	0	0	0
1	0	O	0
2	1	11	1
3	1	1	1
4	2	2	2
5	2	2	2
:	:	:	
250	146	146	146
251	147	147	147
252	147	147	147
253	148	148_	148
254	148	148	148
255	149	149	149

FIG. 11

LUTB FOR MONOTONE FILTERING

INDEX	ROUTB	GOUTB	BOUTB
0	0	0	0
1	0	0	0
2	0	0	0
3	0	0	0
4	0	0	0
5	0	0	0
:	:		•
250	28	28	28
251	28	28_	28
252	28	28	28
253	28	28	28
254	29	29	29
255	29	29	29

• · ·



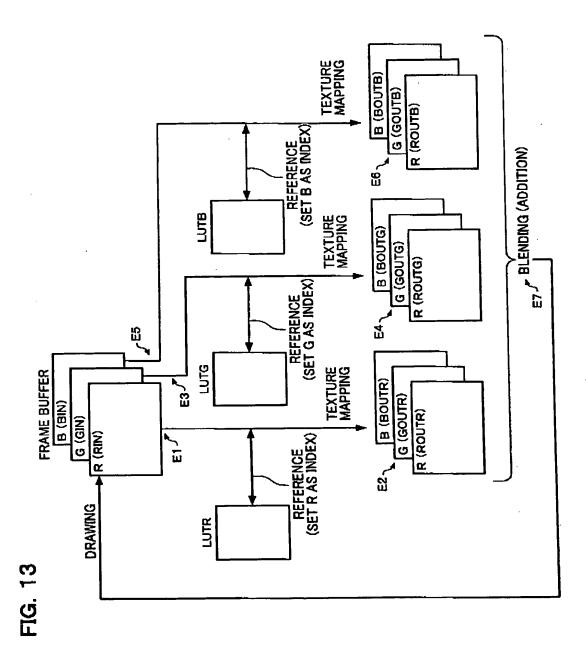




FIG. 14

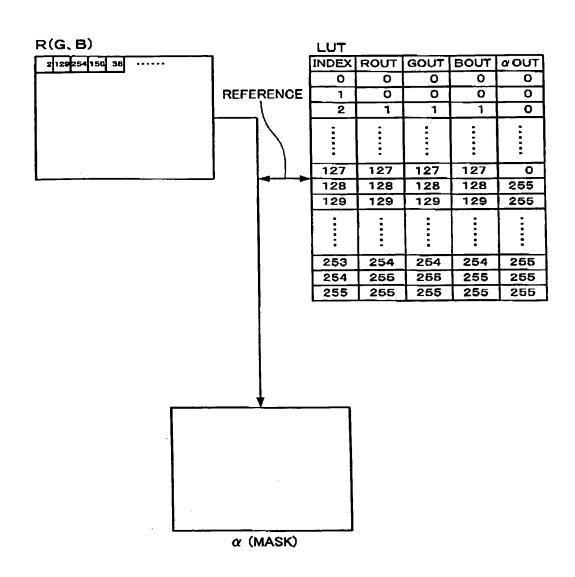
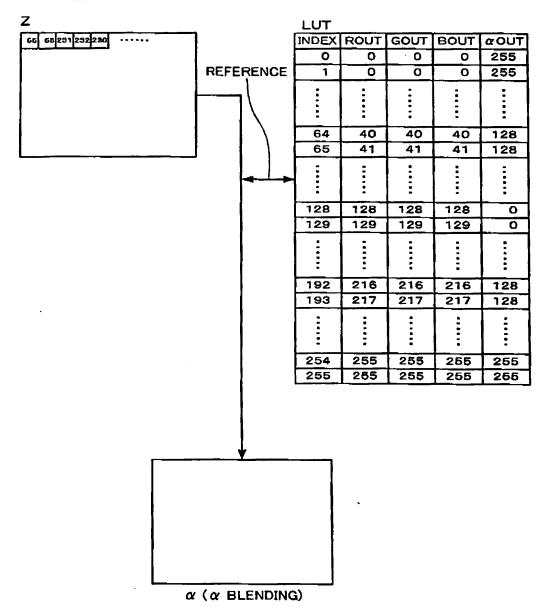
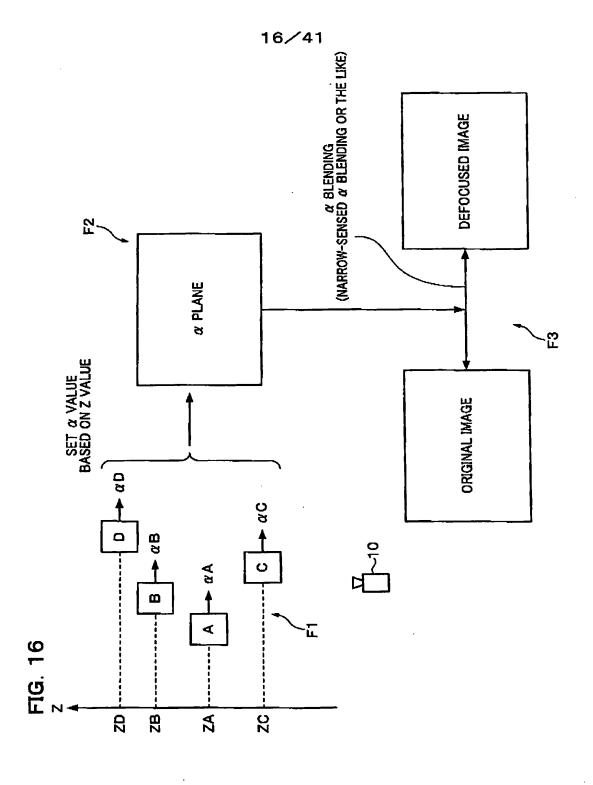
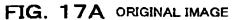


FIG. 15









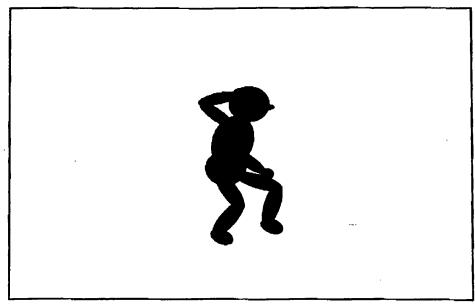
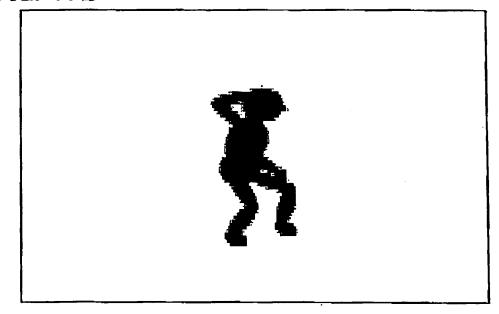


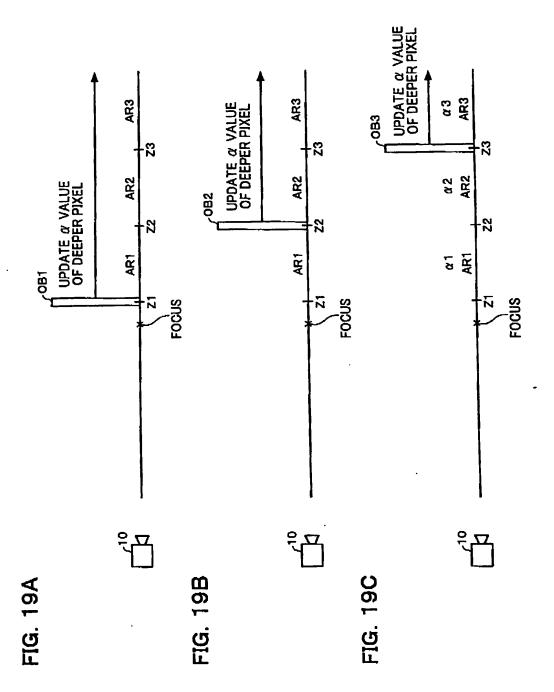
FIG. 17B DEFOCUSED IMAGE



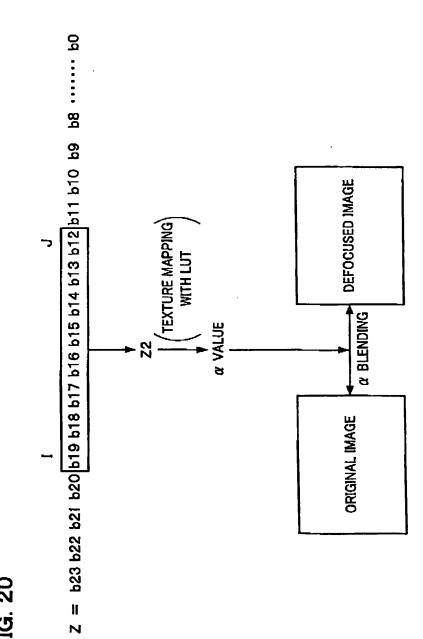
 $\alpha4'=1.0 \mid \alpha3'=0.7 \mid \alpha2'=0.4 \mid \alpha1'=0.2 \mid \alpha0=0.0 \mid \alpha1=0.2 \mid \alpha2=0.4 \mid \alpha3=0.7 \mid \alpha4=1.0$ AR4 AR3 **Z**3 AR2 AR1 AR1 72, AR2 AR3' AR4

IG. 18

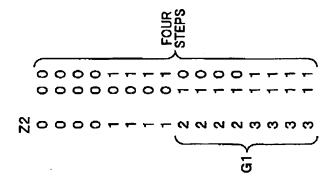


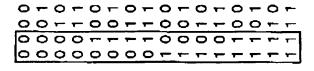


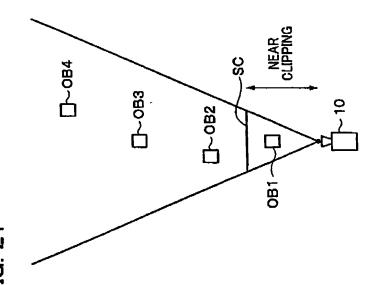
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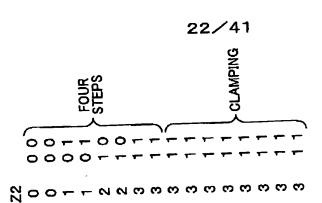


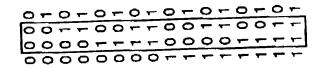
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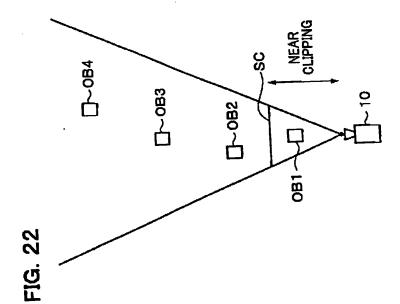


FIG. 23

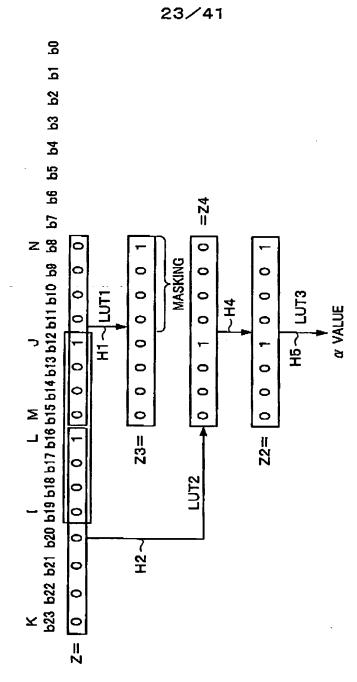


FIG. 24

LUT1 (BITS 15 TO 8)

INDEX	OUT (ANY ONE OF R. G. B) AND α
OxOO (00000000)	0x00 (0000000)
0x0F (00001111)	0x00 (00000000)
0x10 (00010000)	0x01 (00000001)
0x1F (00011111)	0x01 (00000001)
0x20 (00100000)	0x02 (00000010)
0x2F (00101111)	0x02 (00000010)
0x30 (00110000)	0x03 (00000011)
0xE0 (11100000)	OxOE (00001110)
OxEF (11101111)	0x0E (00001110)
OxFO (11110000)	OxOF (00001111)
OxF1 (11110001)	OxOF (00001111)
0xF2 (11110010)	0x0F (00001111)
OxFF (1111111)	OxOF (00001111)

FIG. 25

LUT2 (BITS 23 TO 16)

	INDEX	OUT (ANY ONE OF R. G. AND α	В
0x00	(00000000)	0000000	0)
0x01	(00000001)	0x10 (0001000	0)
0x02	(00000010)	0x20 (0010000	0)
0x03	(00000011)	0x30 (0011000	0)
0×04	(00000100)	0x40 (0100000	0) .
OxOE	(00001110)	OxEO (1110000	0)
0x0F	(00001111)	0xF0 (1111000	0)
0x10	(00010000)	OxFO (1111000	0)
0x11	(00010001)	OxFO (1111000	
			CLAMPING
0xFF	(11111111)	0xF0 (1111000	00)

FIG. 26A

LUT3

INDEX		OUT(α)	
0x00	(00000000)	OxFF (11111111)	
0x01	(00000001)	OxFE (11111110)	
0x02	(00000010)	0xFB (11111011)	
0x7F	(01111111)	0x00 (00000000)	
0x80	(10000000)	0x00 (0000000)	
0x81	(10000001)	0x00 (0000000)	
OxFE	(11111110)	OxFE (11111110)	
OxFF	(11111111)	OxFF (11111111)	

FIG. 26B

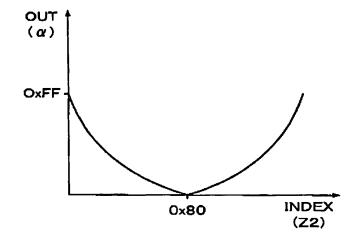


FIG. 27

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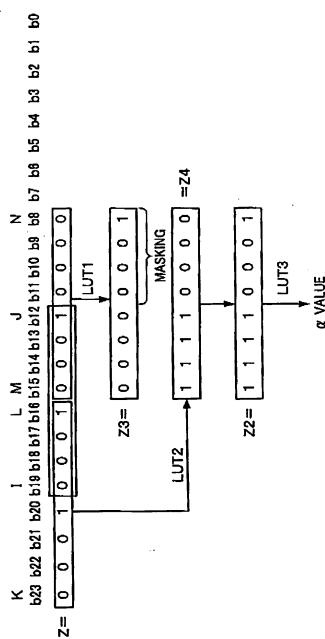
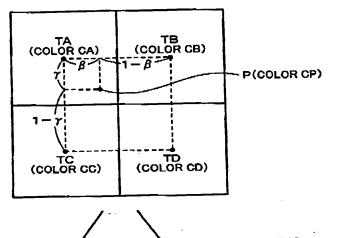
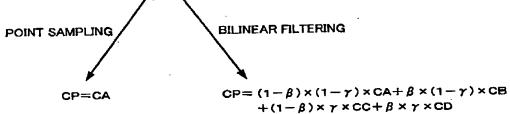
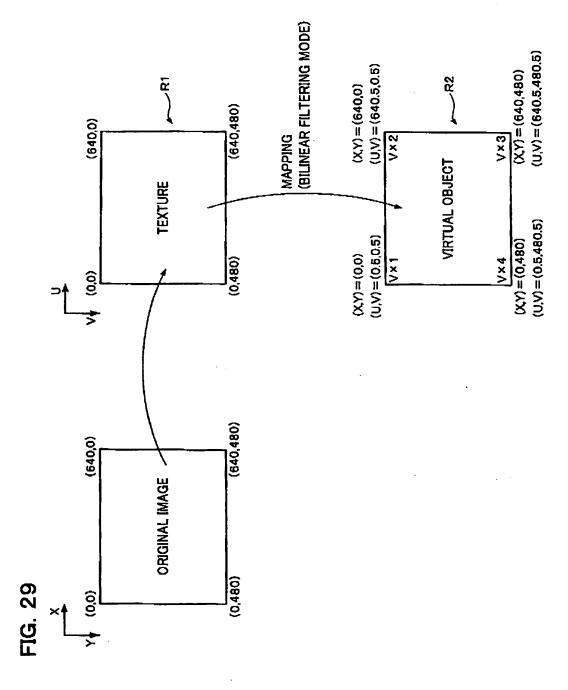


FIG. 28







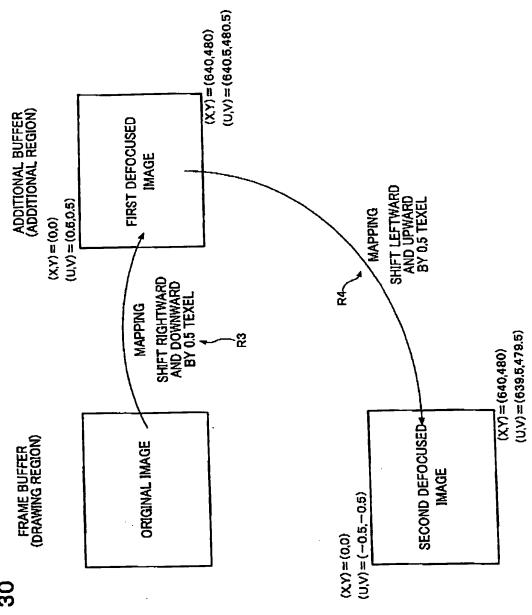


FIG. 30

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FIG. 31A

THE STATE OF THE S

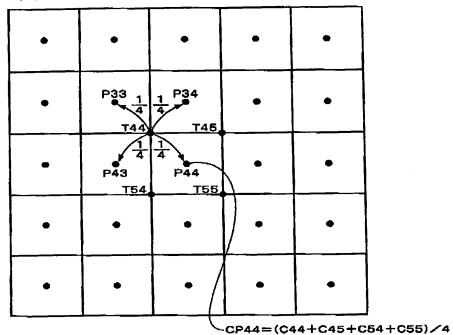
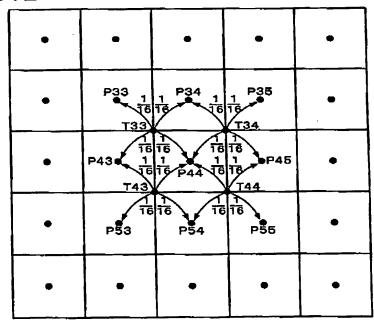


FIG. 31B



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FIG. 32A

<u>1</u> 16	<u>2</u> 16	1 16	·
<u>2</u> 16	4 16	<u>2</u> 16	
1 16	<u>2</u> 16	116	

FIG. 32B

<u>1</u>	<u>4</u>	<u>6</u>	<u>4</u>	<u>1</u>
256	256	256	256	256
4	16	24	16	<u>4</u>
256	256	256	256	256
6	24	36	24	<u>6</u>
256	256	256	256	256
4	16	24	16	<u>4</u>
256	256	256	256	256
<u>1</u>	<u>4</u>	6	<u>4</u>	1
256	256	256	256	256



FIG. 33

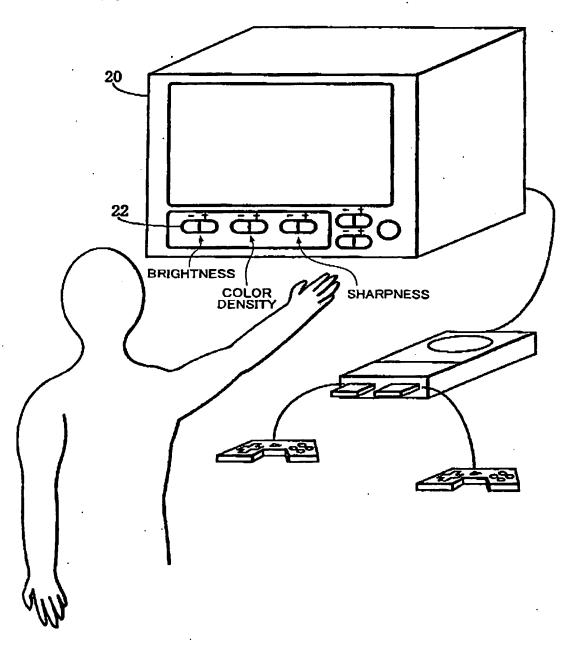
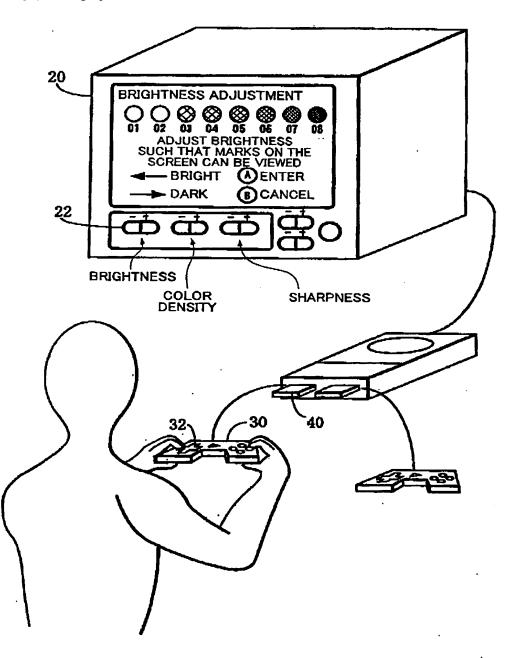






FIG. 34







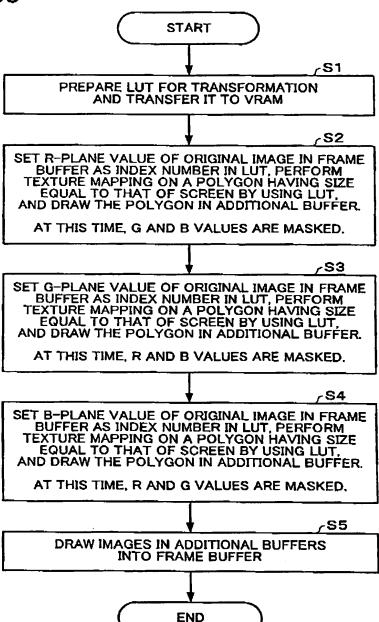




FIG. 36

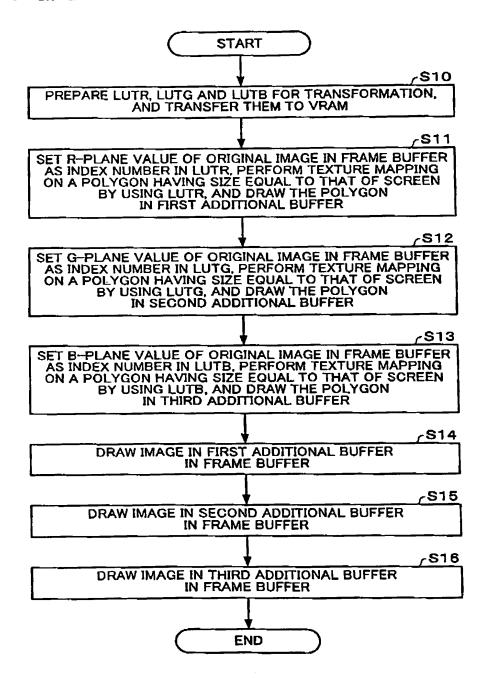
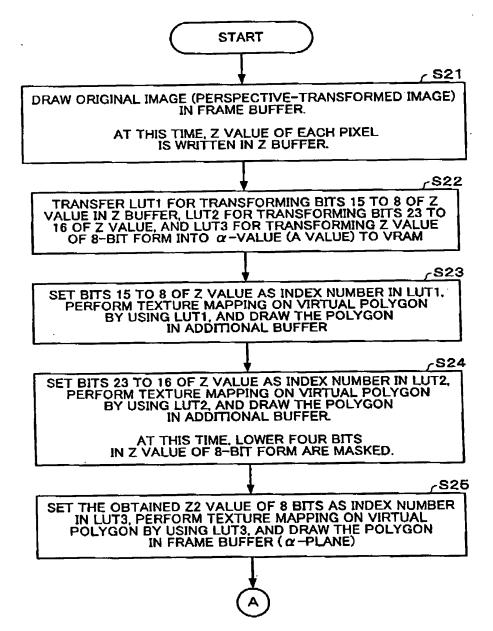




FIG. 37



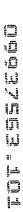
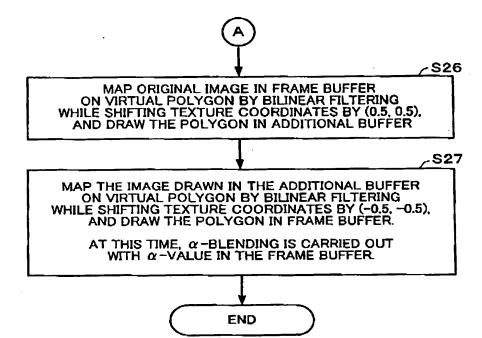
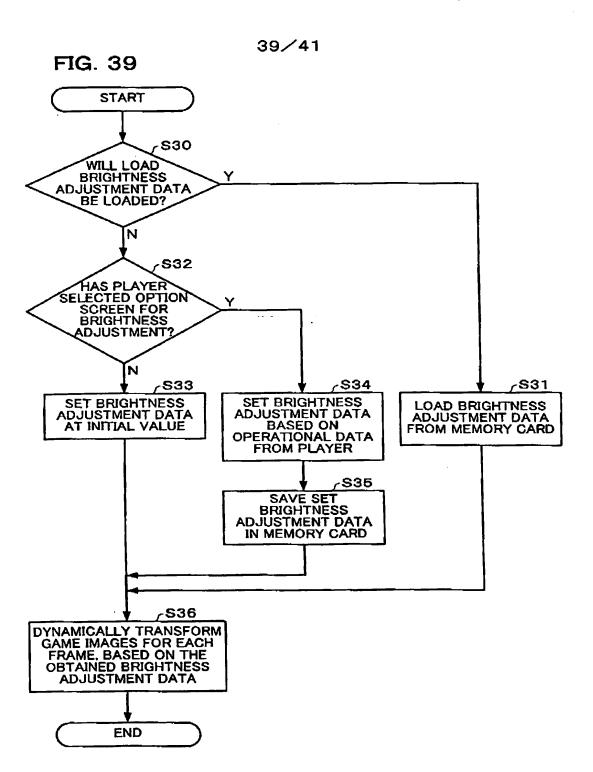




FIG. 38





COMMUNI-CATION INTERFACE NETWORK 930 982 980 SOUND PROCESSOR CD DRIVE မ 970 924 DMA FRAME BUFFER TEXTURE STORAGE -912 910 096 DRAWING DISPLAY RAM 905 906 904 950 DATA EXPANDING PROCESSOR GEOMETRY PROCESSOR COPROCESSOR ROM 944 MEMORY CARD 900 940 PROCESSOR SERIAL INTERFACE MAIN GAME CONTROLLER

FIG. 40

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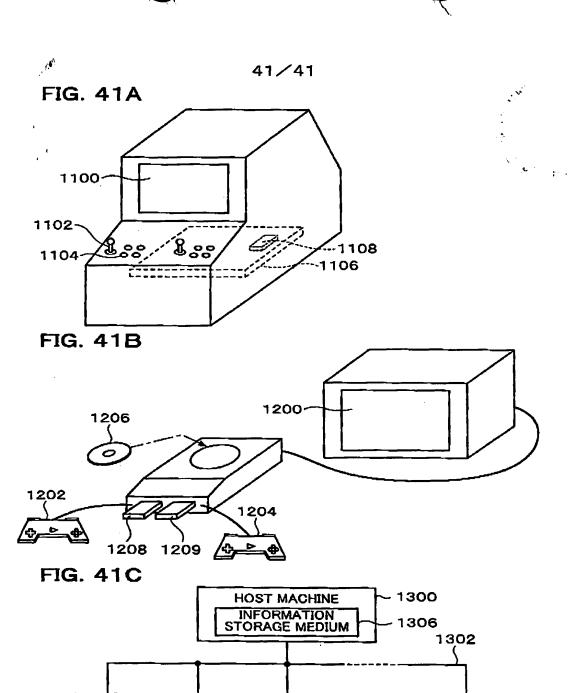
TERMINAL

1304-1

TERMINAL

1304-2

. 4.



TERMINAL

1304 - 3

TERMINAL

1304-n